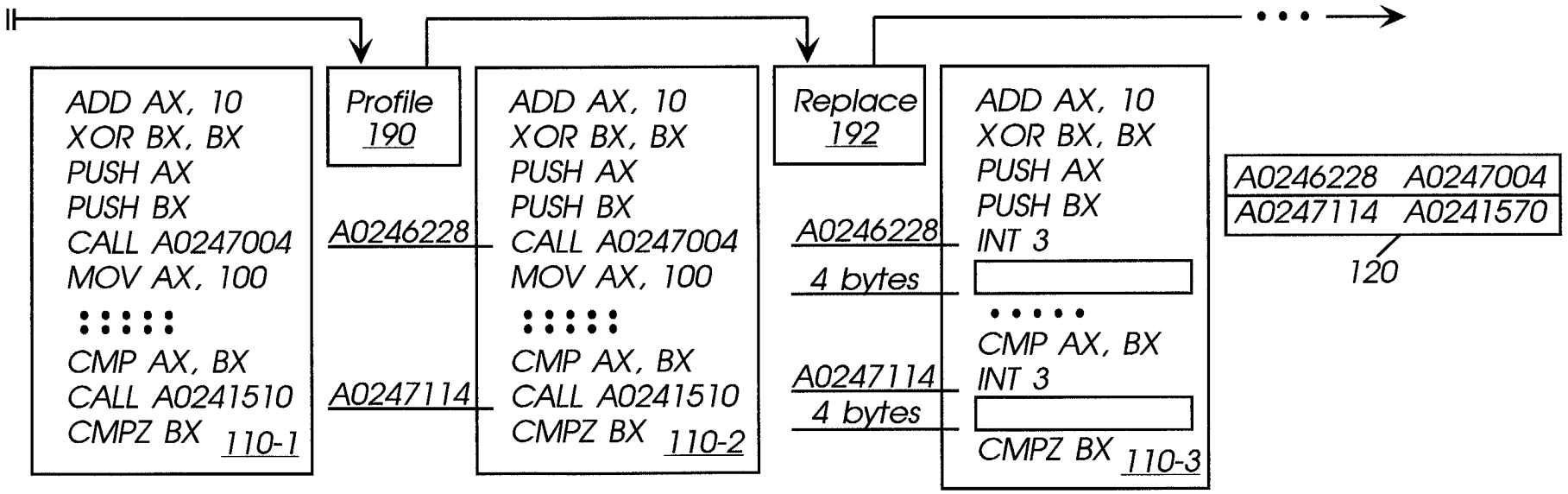
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
CALL A0247004
MOV AX, 100
CMP AX, BX
CALL A0241510
CMPZ BX 110-1
Profile 190
A0246228
A0247114
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
CALL A0247004
MOV AX, 100
CMP AX, BX
CALL A0241510
CMPZ BX 110-2
Replace 192
A0246228
4 bytes
A0247114
4 bytes
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
INT 3
CMP AX, BX
INT 3
CMPZ BX 110-3
A0246228 A0247004
A0247114 A0241570
120

FIG. 1A

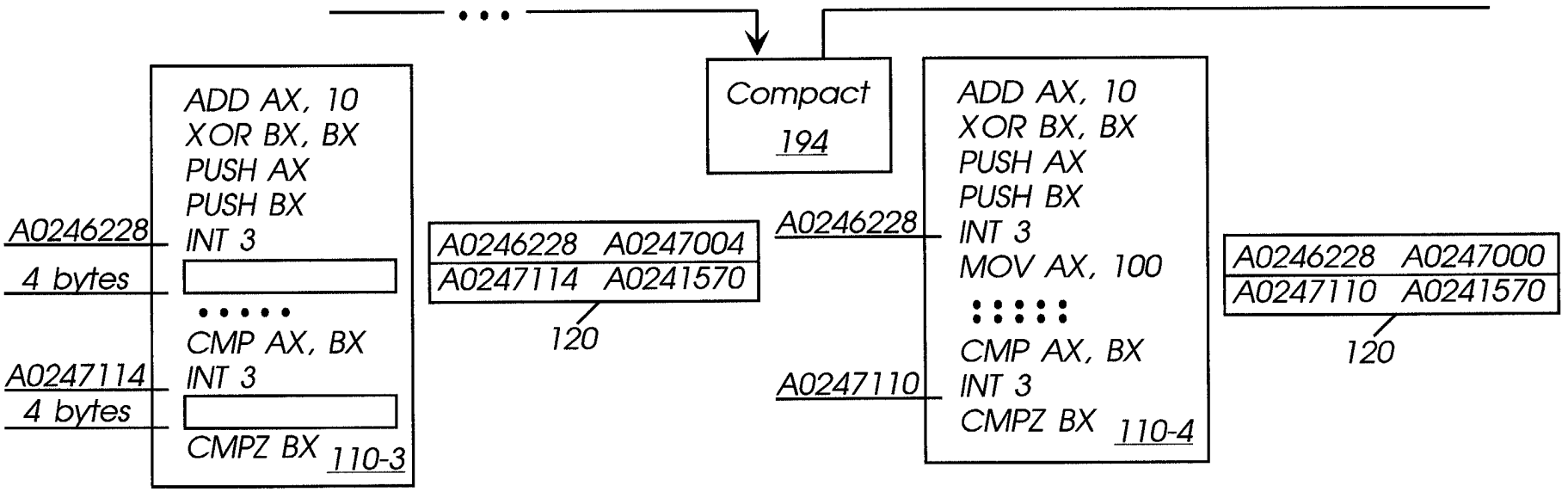
Compact
194
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
A0246228
INT 3
4 bytes
CMP AX, BX
A0247114
INT 3
4 bytes
CMPZ BX
110-3
A0246228 A0247004
A0247114 A0241570
120
ADD AX, 10
XOR BX, BX
PUSH AX
PUSH BX
A0246228
INT 3
MOV AX, 100
CMP AX, BX
A0247110
INT 3
CMPZ BX
110-4
A0246228 A0247000
A0247110 A0241570
120

FIG. 1B

10620 "92h9260

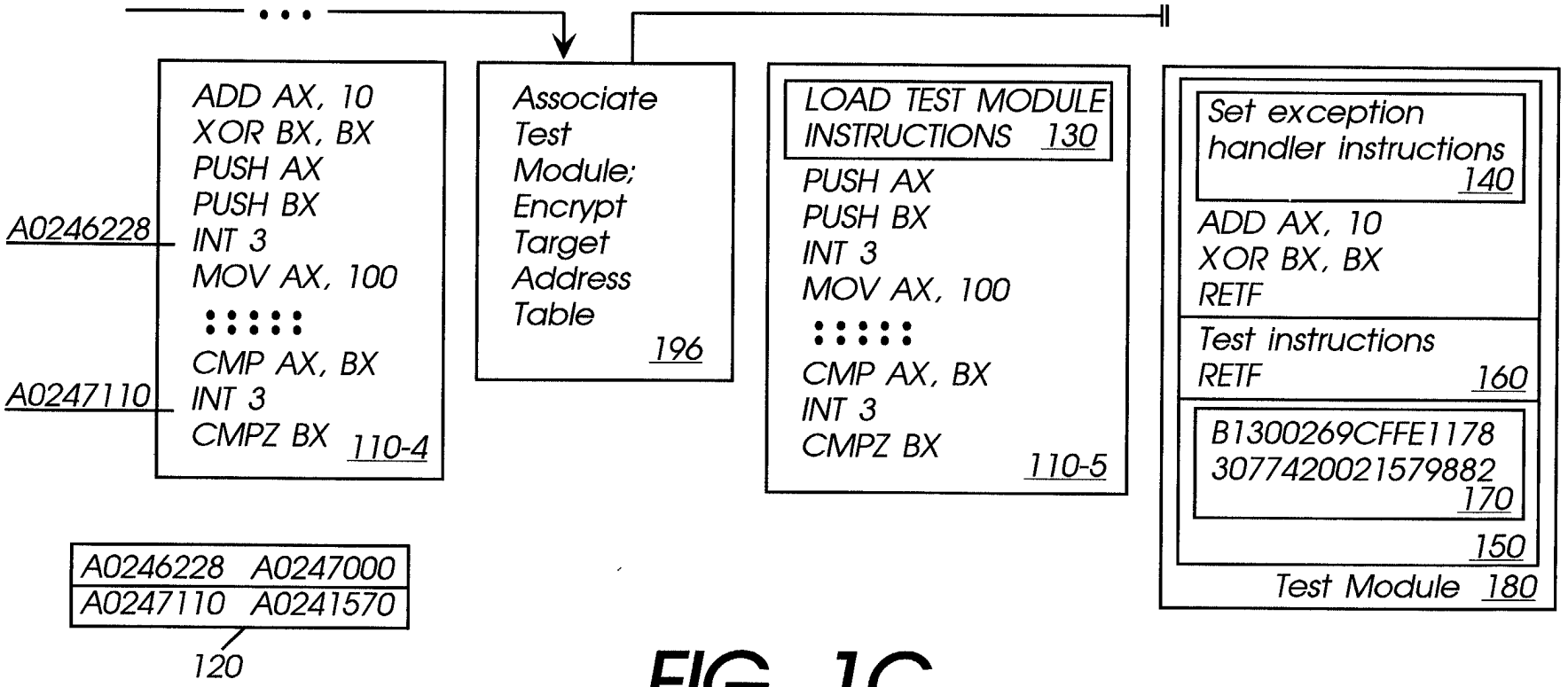


FIG. 1C

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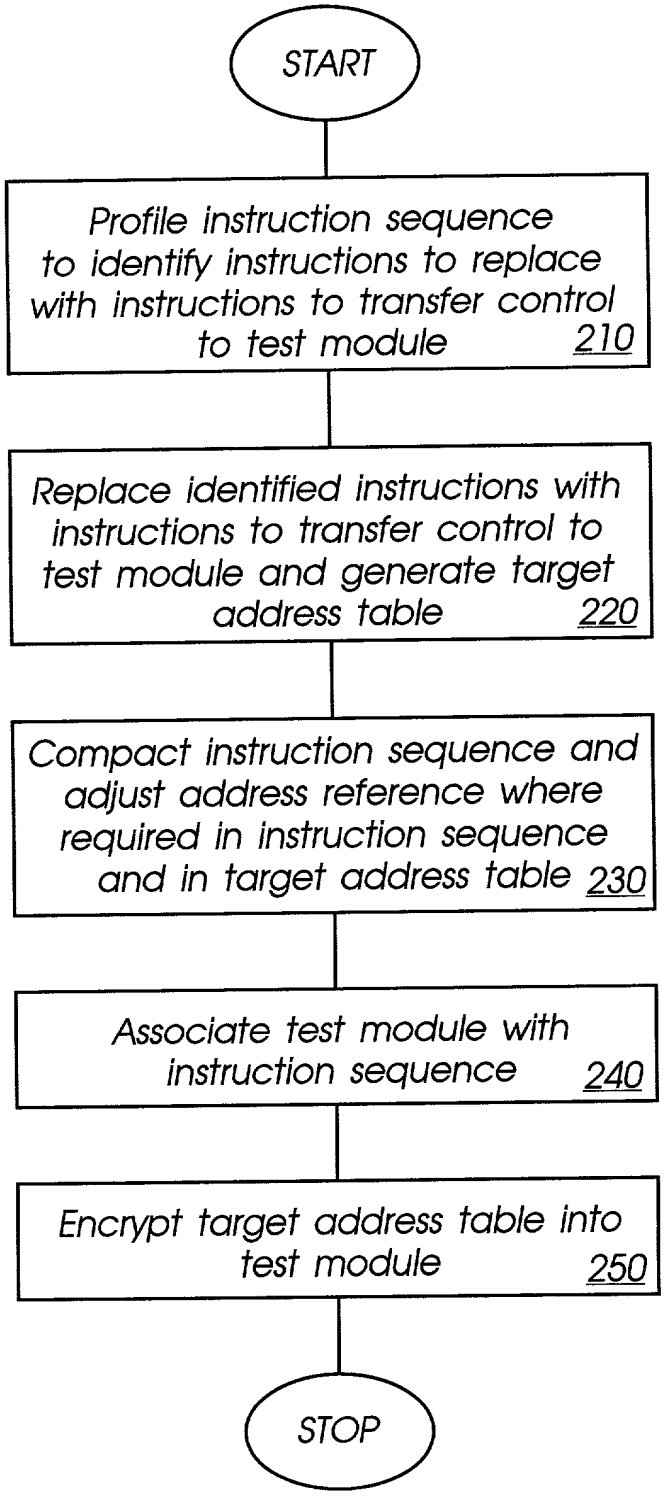


FIG. 2

TOC240 "S2C4B2C60

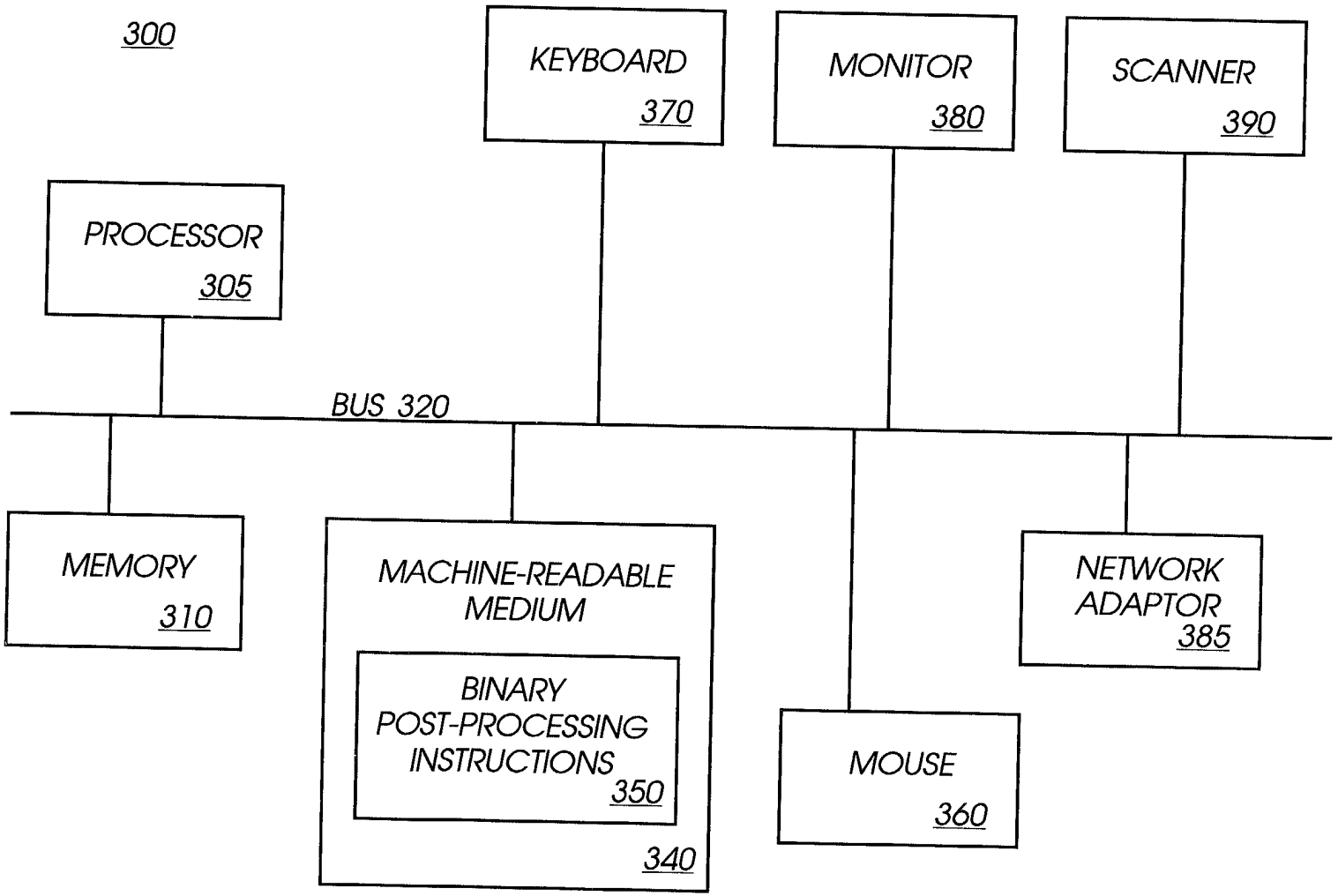


FIG. 3

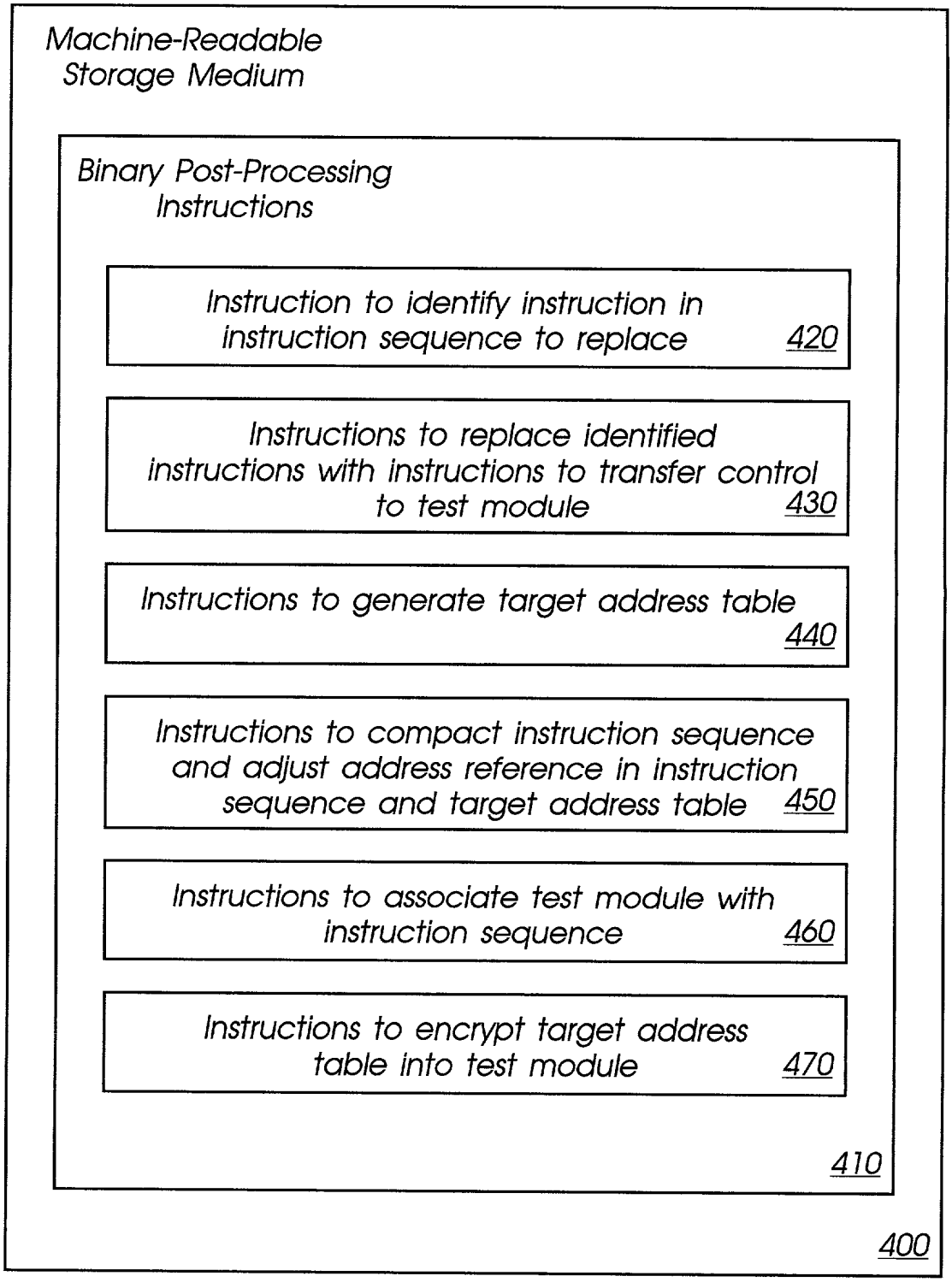
Machine-Readable
Storage Medium
Binary Post-Processing
Instructions
Instruction to identify instruction in instruction sequence to replace 420
Instructions to replace identified instructions with instructions to transfer control to test module 430
Instructions to generate target address table 440
Instructions to compact instruction sequence and adjust address reference in instruction sequence and target address table 450
Instructions to associate test module with instruction sequence 460
Instructions to encrypt target address table into test module 470
410
400

FIG. 4

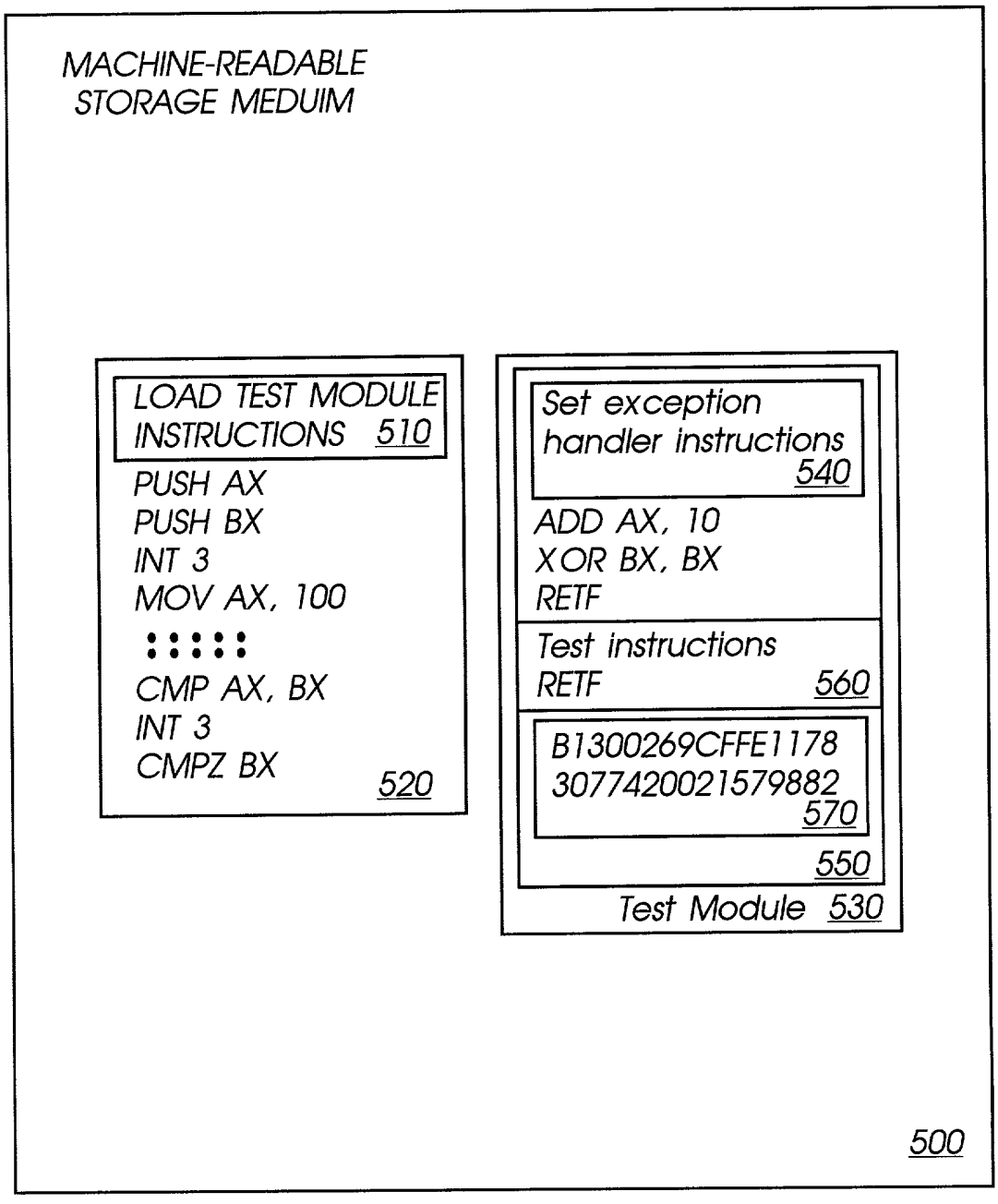
MACHINE-READABLE
STORAGE MEDUIM
LOAD TEST MODULE
INSTRUCTIONS 510
PUSH AX
PUSH BX
INT 3
MOV AX, 100
:::::
CMP AX, BX
INT 3
CMPZ BX
520
Set exception
handler instructions
540
ADD AX, 10
XOR BX, BX
RETF
Test instructions
RETF
560
B1300269CFFE1178
3077420021579882
570
550
Test Module 530
500

FIG. 5